



Col C

PATENT  
Customer No. 22,852  
Attorney Docket No. 06720.0070

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent No.: 6,750,515 B2 )  
Inventors: Ming-Dou Ker et al. )  
Issue Date: June 15, 2004 )  
For: SCR DEVICES IN SILICON-ON- )  
INSULATOR CMOS PROCESS FOR )  
ON-CHIP ESD PROTECTION )

**Certificate**  
**SEP 21 2004**  
**of Correction**

Commissioner for Patents  
P.O. Box 1450, Alexandria, VA 22313-1450

Sir:

REQUEST FOR CERTIFICATE OF CORRECTION

Pursuant to 35 U.S.C. 254 and 37 C.F.R. 1.322, this is a request for the issuance of a Certificate of Correction in the above-identified patent. Two (2) copies of PTO Form 1050 are appended. The complete Certificate of Correction involves one (1) page.

The mistakes identified in the attached form occurred through the fault of the Patent Office, as clearly disclosed by the records of the application which matured into this patent. Issuance of the Certificate of Correction containing the correction is earnestly requested.

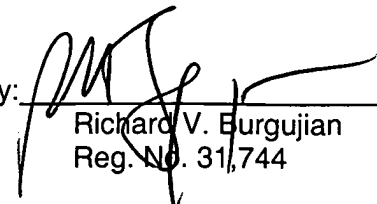
If it should be determined that any of the mistakes resulted from an error made in good faith by the applicants, then, pursuant to 35 U.S.C. 255 and 37 C.F.R. 1.323, it is requested that a Certificate of Correction be issued correcting such mistakes. Under such circumstances, it is requested that the fee set forth in 37 C.F.R. 1.20(a) and any additional fees needed be charged to our Deposit Account No. 06-0916, for which authorization is hereby given.

09/17/2004 RFEKADU2 00000069 060916 6750515

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Respectfully submitted,  
FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

Dated: 9/15/04

By:   
Richard V. Burgujian  
Reg. No. 31,744

SEP 22 2004

**UNITED STATES PATENT AND TRADEMARK OFFICE**  
**CERTIFICATE OF CORRECTION**

PATENT NO. 6,750,515 *B2*  
DATED: June 15, 2004  
INVENTORS: Ming-Dou Ker et al.

It is hereby certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 1, Col. 9, Line 22, "n-type portions and" should read --p-type portions and--.

Claim 1, Col. 9, Line 25, "n-type portion and" should read --p-type portion and--.

MAILING ADDRESS OF SENDER

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Patent No: 6,750,515 *B2*

No. of additional copies  
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**SEP 22 2004**

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